

# News Release

## Research team led by Hitachi Cambridge Laboratory develops highly-sensitive readout detector for Si-CMOS based quantum computer

*Moving one step closer to the realisation of a Si-based quantum computer*

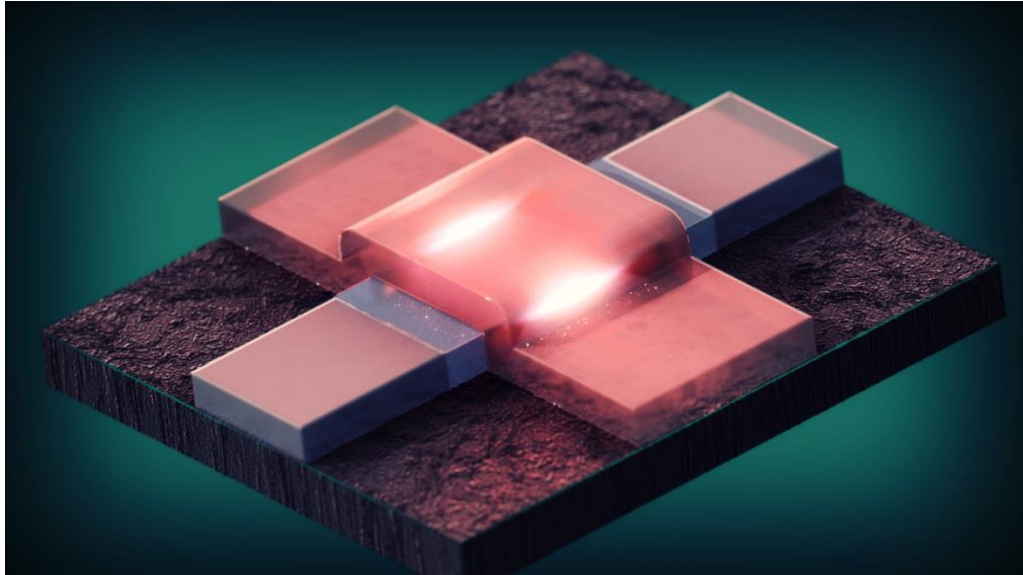


Figure 1. Graphical representation of a Silicon CMOS transistor used in the experiment

London, July 20, 2018 - Hitachi Europe Ltd., a wholly owned subsidiary of Hitachi, Ltd. (TSE: 6501, "Hitachi") today announced that researchers at the Hitachi Cambridge Laboratory ("HCL"), working in collaboration with academic partners at the University of Cambridge, University College London, and CEA-LETI ('the team'), have successfully demonstrated a highly-sensitive detector to readout information from Si-CMOS based quantum bits (qubits).

The new detector, with a charge sensitivity of  $1.3 \mu\text{e}/\sqrt{\text{Hz}}$ , is five times more sensitive than the silicon radio-frequency single-electron transistor which has been the most sensitive technology for silicon-based quantum computers up to now.<sup>\*1</sup> The successful demonstration of the new detector represents another step forward towards the realisation of a Si-CMOS based spin quantum computer.<sup>\*2</sup>

Quantum computers promise to solve some of the most challenging computational problems such as simulation for revolutionary new materials, chemicals and medicines. Research on quantum computation has demonstrated that it is now possible to build small quantum processors in a variety of hardware platforms, and their computational capabilities are approaching those of the most powerful supercomputers. However, to tackle the most demanding computational simulations, quantum computers will need a much larger number of qubits than what current technologies can provide.

Currently, qubits are wired one by one in a very similar fashion to what was done for the first electronic computers built with discrete components, but this approach will become unsustainable as quantum processors become increasingly complex.

To solve the wiring challenge, the team is developing Si-CMOS based qubits and integrating them with digital electronics so that in the future, complex quantum processors can be managed with a small number of input/output lines.<sup>\*3</sup> Silicon, the base material of large-scale integration technology, is amongst the most promising candidates for large-scale quantum computing because qubits based on the spin of a single electron can retain quantum information for much longer than any other solid-state implementation.

This time, the team focused on improving the readout circuitry of the Si-CMOS based quantum computer. The team designed a detector that combines Si-CMOS technology and superconducting high-frequency components to detect single-electrons moving in the quantum device. The results revealed an improvement in the sensitivity of a factor of 30 over previous designs<sup>\*2</sup> making it five times more sensitive than the best reported readout detector for silicon-based quantum computers.<sup>\*1</sup>

This research was carried out at HCL in collaboration with academic partners at the University of Cambridge, University College London and CEA-LETI, France, and was supported by funding from the European Union's Horizon 2020 research and innovation programme under grant agreement No 688539: MOS-Quito project.<sup>\*4</sup> The results have just been published in the 19<sup>th</sup> July 2018 issue of Physical Review Applied, as an Editor's suggestion.

<sup>\*1</sup> S. Angus et al. *A silicon radio-frequency single-electron transistor*, App. Phys. Lett. 92 112103 (2008).

<sup>\*2</sup> M.F. Gonzalez-Zalba et al. *Probing the limits of gate-based charge sensing*, Nat. Commun. 6 6084 (2015)

<sup>\*3</sup> S.Schaal et al. *Conditional Dispersive Readout of a CMOS Single-Electron Memory Cell*, Phys. Rev. App 9 054016 (2018)

<sup>\*4</sup> MOS-Quito Project website <https://www.mos-quito.eu>

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The Hitachi Cambridge Laboratory (HCL) was established through collaboration between the Cavendish Laboratory of the University of Cambridge, and the Research & Development Group of Hitachi, Ltd. The areas of research activity in HCL include microelectronics, magnetism, optoelectronics and semiconductor physics, with view to creating new concept advanced electronic and optoelectronic devices. Currently research projects are being pursued in the areas of Quantum information and Spintronics. For more information, visit <http://www.hit.phy.cam.ac.uk>  
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